Reducing Build Time Through Precompilations for Evolving Large Software

Yijun Yu
University of Toronto

Homayoun Dayani-Fard
IBM Canada

John Mylopoulos
University of Toronto

Periklis Andritsos
University of Toronto

Abstract

Large-scale legacy programs take long to compile, thereby hampering productivity. This paper presents algorithms that reduce compilation time by analyzing syntactic dependencies in fine-grain program units, and by removing redundancies as well as false dependencies. These algorithms are combined with parallel compilation techniques (compiler farms, compiler caches), to further reduce build time. We demonstrate through experiments their effectiveness in achieving significant speedup for both fresh and incremental builds. The presented algorithms can also apply to reducing the dependencies in web services. The comparison is conducted with coarse-grain file-level optimizations or heavy overhead dependency analysis techniques.

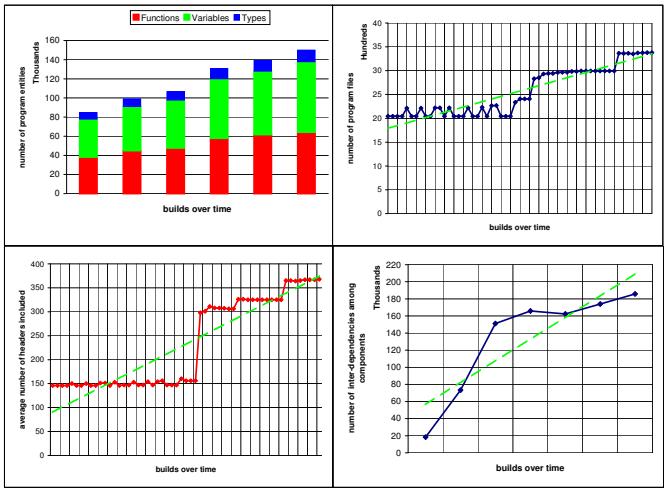

Figure 1. The growth of an industrial software

1 Introduction

Managing complexity of large-scale software development is central to software engineering [39]. Software systems, under maintenance pressures for improved functionality, better quality and more services, are becoming more complex by the Lehman's 2nd laws of evolution [27]. However, such pressures obscure the internal structure and quality of the software, and make it difficult to understand and maintain [11]. This work was motivated by a study that we performed on a number of commercial software systems, where the growing trend had resulted in long compilation times, unnecessary compilations, and increasing inter-dependencies. The solution, can broadly be stated as an automatic approach to improving the structure of software systems. For example, an evolving large-scale C/C++ software in IBM has been observed a growth (Figure 1) of the number of program entities (broken down into functions, variables and types), source files, included header files, and component dependencies for several major releases of the software. The number of files in the system has been growing steadily for the past four years, with jumps near major new releases. Similarly, the number of actual dependencies have grown as well as the average number of header files included by program files.

A software system typically consists of many *compilation* and *header units*. A compilation unit (e.g. '.c' file) will be compiled into an object file [26]. It is also called a *translation unit* in the GCC community [19]. A header unit (e.g. '.h' file) will be included into a compilation unit prior to compilation. At a small granularity, we define a *program unit* (PU) as a declaration or definition of a symbol. User-defined symbols must be *defined* once globally in the program, but can be *declared* multiple times in different compilation units. In C/C++, global variables and functions are regarded as definition units; the remaining symbols are declarations, such as static functions/variables, function/variable prototypes, classes, structs, unions, typedefs, enumerators, etc. Most declarations are grouped into the headers and *preprocessed* into the compilation unit by replacing the `#include` directives with the content of corresponding headers. A full expansion of the `#include` directives results in a *preprocessed image*.

Introducing headers generally reduces spatial redundancies to save space as well as to reduce update inconsistencies. However, headers can be included by multiple files and as such may contain declarations that are *falsely* needed by

some of the compilation units that include them. Thus, *redundancies* and *false dependencies* may be introduced into the preprocessed compilation units. Although the functionality of a system is not affected, they do affect the efficiency of the development process: the longer the build process (e.g., compilation and linking), the longer developers have to wait in order to integrate their changes. Such problems are exacerbated in sync-and-stabilize developments [10], where program changes are integrated into the code-base nightly as the product is built. Borison [8] suggested that '*most recompilations after a change to an interface (headers) are redundant and that this redundancy is a direct consequence of how we modularize software systems*'. An observation was made that '*6 and 9 out of every 10 compilations are unnecessary costs*'.

Concerning the efficiency in a build process, the declaration redundancies slow down the compilation of individual compilation unit where they occur; the false dependencies require extra recompilation of the compilation units that do not need to include the changed part.

This paper presents a *precompilation* (i.e. source-to-source transformation before compilation) approach to the removal of redundancies inside compilation units and false dependencies among the files. We show that removing declaration redundancies within compilation units can speedup the *fresh* builds (i.e., compiling everything from scratch), while removing false dependencies can speedup the *incremental* builds (i.e. recompiling what has changed). Unlike file level dependency checking, our approach analyzes the dependencies among *fine-grain* program units inside headers and compilation units. Furthermore, to reduce the overhead of using the exact dependencies, we adopt an approach that needs *light-weight* amount of information to be extracted. The resulting precompilation technique is transparent to the build process, incremental to the development and independent of the choice of the compiler. In addition to proposing algorithms for identifying and removing redundancies and false dependencies, the paper also presents a tool that has been experimentally evaluated.

The rest of the paper is organized as follows. Section 2 presents definitions and algorithms that serve as foundations for our approach. Section 3 outlines experimental results when applied to a public-domain software (VIM [29]) in C as well as an industrial component in C++. Section 4 describes related work in the compilation optimization area and compares them with this work. Section 5 provides some concluding remarks.

2 Our approach

Our process consists of four steps: 1) the compilation units are parsed by an adapted GCC 3.4.0 parser into sequences of program units; 2) a redundancy removal algorithm is applied to remove unnecessary PUs by traversing the abstract syntax tree (AST) once; 3) a partitioning algorithm is performed on the lexically ordered necessary PUs to regroup them into headers and compilation unit source files, while preserving their dependency order; 4) finally, a logical grouping of files is created by automatic clustering of the generated compilation units to reduce the number of generated headers. In the remainder of this section we present each step of our approach in detail.

Extracting program units A unit u is *lexically* before unit v, if u occurs before v in one of the preprocessed images. A program unit u *depends on* another program unit v if u uses v and v occurs lexically before u.

The program unit extraction is an algorithm implemented by adapting the GCC compiler: given a compilation unit as a sequence of tokens (the lexicon stream) from the lexical analyzer, the algorithm converts it into a sequence of program units. The token strings are concatenated into a character stream, which is recorded upon the identification of a program unit and reset to empty for the next token. This allows us to accurately locate program units by their start/end lines and columns. The input to the precompiler is a sequence of tokens as a result of the lexical analysis. Each token can be associated with a string. The algorithm converts the lexicon stream into a stream of program units.

Initially the patched lexical analysis in GCC concatenates the input code into a character stream, which is reset to empty as a program unit is identified. In this manner, we can accurately locate the program units by the exact start/end line and column number. Moreover, since we embedded the code extracted with the program units, line numbers do not need to be maintained.

Based on the stream, the GCC parser constructs an abstract syntax tree T. During the parsing, we are interested in the tree nodes that may be identified as program units. The `-fdump-translation-unit` option in GCC is not sufficient for this purpose because external references are not stored in the abstract syntax tree. Thus we adapted the GCC type-checker that calls the `build_external_ref` routine to keep track of the dependency of the program unit on the externally referenced declaration unit. The output of one compilation unit is a sequence of program units P. Each program unit in P has an associated code segment and at least one node in T. On the other hand, each declaration node corresponds to at most one program unit. The new option in our adapted GCC is called `-fdump-program-unit`.

Reusing the balanced binary tree `splay_tree` data structure in GCC, the resulting PU sequence is stored efficiently: a find/update operation on the program units takes $\mathcal{O}(\log_2 |P|)$ basic operations where $|P|$ is the number of the program units. Let $|T|$ be the number of the

Algorithm. ParsingIntoProgramUnits
Input `stream`: A sequence of tokens in a compilation unit
Output P: a sequence of PUs and C: the set of definitions
$n = 1$;
for each $t \in$ T being parsed by the YACC *parser*
if t is a definition **then**
$P[{+}{+}n] = \{t\}$; $C = C \cup \{P[n]\}$; t.key = n;
Code[n] = stream; stream = "";
else if t is an alias **then**
$P[n] = P[n] \cup \{t\}$; t.key = n;
Code[n] += stream; stream = "";
else if t is a declaration **then**
$P[{+}{+}n] = \{t\}$; $H = H \cup \{P[n]\}$; t.key = n;
Code[n] = stream; stream = "";
endif
if $t \in$ build_external_ref **then**
DependencyExtraction(t)
end if
end for

Algorithm. DependencyExtraction
Input A tree node t in the AST T
Output Program units required by t
if t has not been visited **then**
if $t.key > 0$ **then**
set $P[t.key]$ as necessary;
set t as visited;
if t is a typedef **then**
DependencyExtraction(TREE_DECL(t));
else if t is a struct, a class or a union **then**
DependencyExtraction(FIELDS(t));
else if t is a function **then**
DependencyExtraction(PARAMS(t));
... /* other dependencies */
end if
end if
end if

abstract syntax tree nodes, the parsing of the lexical tokens takes $\mathcal{O}(|T| \log_2 |P|)$ time and the book keeping for looking up declaration for external references takes also $\mathcal{O}(|T|)$ operations. Therefore, the overall time complexity is $\mathcal{O}(|T| \log_2 |P|)$.

Since the original parser in GCC removes these dependency relations from the abstract syntax tree after parsing, we need to keep them along with the parsing process before they get lost. While parsing a program unit definition, the abstract syntax tree of a referenced expression (such as a typedef, a field or an enumerator reference, a parameter declaration, a function call or a variable declaration, etc.) triggers the dependency on its declararation unit.

Thus the dependency extraction algorithm takes $\mathcal{O}(|T(t)|)$ elementary operations for the additional bookkeeping operations, where $|T(t)|$ is the number of tree nodes rooted by a definition unit t in the AST T constructed by the parsing procedure. Thus it only takes $\mathcal{O}(|T|)$ operations.

We illustrate the parsing process by an example compilation unit, which is dissected into a sequence of 7 program units. Each has a character stream until the next program unit. An alias shown in the comment is associated with the kind and the name of a program unit. E.g., "struct:A" indicates a "struct" with a name "A".

```
typedef int NUMBER;           //PU@1 type:NUMBER
struct node;                  //PU@2 forward:node@2
typedef struct node {         //PU@3 type:list@3
 float value;                 //     struct:node@3
 struct node* next;           //        <- PU@3, PU@2
} *list;                      //
struct A {                    //PU@4 struct:A
 union {                      //
   NUMBER value;              //        <- PU@1
 } u;                         //
};                            //
extern int                    //
printf(char *format,...);     //PU@5 funcdcl:printf@5
enum {                        //PU@6 enum:<anonymous>@6
  Satisfied,                  //     enumerator:Satisfied@6
  Denied,                     //     enumerator:Denied@6
};                            //
int main(argc, argv)          //PU@7 funcdef:main@7
int argc; char **argv;        //
{                             //
  list l, n;                  //        <- PU@3
  for (n = l; n; n=n->next)   //
    printf("%f", n->value);   //        <- PU@5
  return (int) Satisfied;     //        <- PU@6
}                             //
```

Several aliases may refer to the same program unit because they do not separate from each other, e.g. the `struct:node` and `type:list` are aliases to `PU@4`. Aliases help the parser to link partial information, such as the enumerator constant `Satisfied`, to the declaration of the anonymous `enum` type.

Entities such as field names, parameter names and auto variables are not considered program units because they are not needed for parsing other program units. E.g., `union:u` inside `struct:A` is not considered a program unit. In this manner, much fewer entities need to be recorded compared to the traditional fact extraction.

The dependencies are extracted for symbols that were previously identified as a program unit, e.g. the PU "7" depends on 3 previously defined PU's $\{3, 5, 6\}$. The output is thus a lexically ordered sequence of program units.

A compiler such as GCC 3.4.0 can have 36 phases from parsing source code into generating hardware instructions. We stop right after the first parsing phase using the option `-fsyntax-only` to have a quicker precompilation, while the other phases will be called in the compilation phase of the precompiled code.

After the adapted GCC compilation[1], we have obtained a lexically ordered sequence of program units, each associated with a key and a region of the code.

Removing redundancies Among all the program units P, we denote the set of *definitions* C as program units that will be stored in the object file, while the set of *declarations* is defined as $H = P \setminus C$ [26]. A *program unit dependency graph* (PUDG) is a digraph $G(P, D)$, where the vertexes in P represent program units, and the edges in $D \subset H \times P$ represent the dependencies among PUs. Given a lexical order $\prec$, the set of program units in a compilation unit is converted into a sequence P, where $P[i]$ denotes the i-th program unit in the sequence. Now a *light-weight* PUDG (LPUDG) is defined as a digraph $G^{\prec} = (P, \prec)$ implied by the lexical ordering of the sequence $\prec$: $P[i] \prec P[j] \iff i < j$. As not all pairs of the program units in the sequence has dependency between each other, $D \subset \prec$.

For each compilation unit, we keep a sequence of program units (which implies LPUDG) rather than storing the complete PUDG. We will show that having the seemingly less accurate LPUDG is enough for the redundancy removal and also enough for the header restructuring.

[1] A compilation can have many phases from parsing source code into generating hardware instructions. For example, GCC 3.4.0 has 36 phases. We can stop right after the first parsing phase using the option `-fsyntax-only` to have a quick precompilation.

```
Algorithm. RemoveRedundantPU
Input Program units sequence P, including the set of
definitions C and the set of declarations H
Output Necessary program unit sequence (N, C) and code
 for i = 1, n
  if P[i] ∈ C then
   set P[i] as necessary
   for each t in P[i]
    DependencyExtraction(P[i])
   end for
  end if
 end for
 for i = 1, n
  if P[i] is necessary
   Print(Code[i]); /* code generation */
  end if
 end for
```

In the set of declarations H, only a subset $N \subset H$ is necessary for the correct compilation of C, while other declarations $R = H \setminus N$ can be removed. After dependency extraction, the immediately dependent declarations for the i-th program unit $P[i]$ can be identified as $N(i)$. Thus the necessary declarations N is the union of all the declarations that are transitively depended by C. This is done through traversing the extracted PU sequence twice. Initially all the definition units are marked as necessary. Then the first traversal iterates through the PU sequence backward from the end to the beginning, marks all the PU's that are directly depended by a currently necessary unit as necessary. After the traversal, all necessary declarations N and definitions C are marked. The second traversal simply outputs the marked PU's from the beginning to the end. The cost of the redundancy removal is $O(|T|)$ where $|T|$ is the nubmer of nodes in the abstract syntax tree $|T|$. Among a parsed sequence of 7 program units in our example program, H={1–6} are declarations, $C = \{7\}$ is the only definition. Then based on the extracted dependencies, the backward traversal marks $\{3, 5, 6\}$ as necessary for 7, then skips the dependency $1 \rightarrow 4$, and marks $\{2\}$ as necessary for 3. At the end of the traversal, program units $R = \{1, 4\}$ will be removed because the definition in $C = \{7\}$ does not depend on them transitively. It takes $|C|$ *DependencyExtraction* operations on each definition program unit. As we know from the previous algorithm, each *DependencyExtraction* operation traverses the subtree rooted at the tree nodes of the program unit. Thus the algorithm at most traverses the abstract syntax trees of the reduced program to identify all the necessary program units. Unlike this example, the density $((|N| + |C|)/|P|)$ of necessary elements in the real applications tends to be much smaller. The reason is that most system headers contain redundant declarations for different platforms. For example, a single line of declaration for `printf` is necessary in the complete 843 lines of the `stdio.h` if it were included.

Removing false dependencies If the same program unit will be included multiple times in different compilation units, it is better to place it into a constructed header. We have presented elsewhere [40] an algorithm to remove redundancies and false code dependencies based on the heavy weight PUDG extraction. In this paper, we adapted GCC for the program units extraction and the efficient redundancy removal. The new header restructuring algorithm only requires the necessary program unit sequences obtained from individual compilation units.

Given the containing relation between files (headers and compilation units) and program units, we define a *file dependency graph* (FDG) $\mathcal{G} = (\mathcal{F}, \mathcal{D})$ where $\mathcal{F}$ represents the set of files. Each element of $\mathcal{F}$ contains a subset of program units P in the PUDG. The vertices $\mathcal{F}$ are separated into headers $\mathcal{H}$ and compilation units $\mathcal{C}$, then the edges $\mathcal{D} \subset \mathcal{H} \times \mathcal{F}$ are the dependencies. The relation between the PUDG G and the FDG $\mathcal{G}$ is determined by the N-to-1 partitioning mapping $\mathcal{X} : N \times \mathcal{F}$, where each element of $\mathcal{F}$ is a partitioned (disjoint) subset of N.

In a file dependency graph, a dependency between a file with program units $A \subset P$ and a file with program units $B \subset P$ is *false* if there is no PU dependency from any PU $a \in A$ to any PU $b \in B$. In header restructuring, we only consider false dependencies caused by spurious PUs in headers. A remedy to this problem is to split the header so that only true dependencies occur.

We do not split the compilation units as individual definitions because the false dependencies in the compilation units have no impact on the build time. Therefore we keep the existing mapping between definitions and compilation units and replace all definitions C^i in the i-th compilation unit with one node $i \in \mathcal{C}$. Thus the new PUDG have a vertex set $N \cup \mathcal{C}$ where N is the union of the necessary declarations in all the compilation units $\mathcal{C}$.

Each necessary declaration $u \in N^i$ of the i-th compilation unit has a dependency (u, i) in the new LPUDG. After the union of the global declarations in compilation units, we also know a set of compilation units that depends on each $u \in N$: $\mathcal{D}(u) = \{i | u \in N^i\}$. Starting from a naive partitioning where each declaration in H is a separate partition set, we merge the PUs that belong to the same sets of compilation units and update the partitioning $\mathcal{H}$. The resulting partitioning describes the headers to be generated. Now we present the pseudo code of our restructuring algorithm.

Algorithm 1. Header Restructuring

Input: The sequences of necessary declarations N^i for each compilation unit $i \in \mathcal{C}$;

Output: A partition of N where $N = \bigcup_{i \in \mathcal{C}} N^i$ and generated header and compilation units.

begin /* Initializing */ $N = \{\}$; $\forall u \in N : \mathcal{D}(u) = \{\}$;
for each $i \in \mathcal{C} : N = N \cup N^i$; $\forall u \in N^i : \mathcal{D}(u) = \mathcal{D}(u) \cup \{i\}$;
/* Partitioning */ **let** $\mathcal{H} = \{\{u\} | u \in N\}$;
repeat done = true;

for each $A, B \in \mathcal{H}$ and $A \neq B$:
if $\bigcup_{a \in A} \mathcal{D}(a) = \bigcup_{b \in B} \mathcal{D}(b)$ **then**:
$\mathcal{H} = \mathcal{H} \cup \{A \cup B\} \setminus \{A, B\}$; done = false;
for each $k \in \bigcup_{a \in A} \mathcal{D}(a) : \mathcal{H}^k = \mathcal{H}^k \cup \{A \cup B\} \setminus \{A, B\}$;
until done;
/* Generating code for header units (HU) and compilation units (C
for $k = 1, |\mathcal{H}|$: PrintSortHUs(k, $\mathcal{H}^k$, CompareUnit);
for $i = 1, |\mathcal{C}|$: PrintSortCUs(i, N^i, $\mathcal{H}$, ComparePU);
end
CompareUnit(I: program unit sets A, B: $A \neq B$; **O:** $\prec, \succ, or =$ **)**
begin if $\mathcal{D}(A) \supset \mathcal{D}(B)$ **return** $\prec$; **if** $\mathcal{D}(A) \subset \mathcal{D}(B)$ **return** $\succ$; retu
ComparePU(I: program units a, b; **O:** $\prec, =$, or $\succ$ **)**
begin if $\exists k : \mathcal{P}^k[i] = a \wedge \mathcal{P}^k[j] = b$ **return** $i - j$; **return** $a - b$; **er**

When generating code with a set of program units $\mathcal{H}^k$, a proper order is used to sort them into a sequence. In our algorithm, the program units in a generated header are compared using their lexical order if there is a compilation unit in which both of them occur. The header units in a compilation unit are compared using a partial order defined by the set inclusion relationship between their transitive closures: $A < B$ iff $\mathcal{D}(A) \subset \mathcal{D}(B)$. Figure 2 illustrates how the algorithm uses the sequences of necessary program units (a) to derive a set of headers and to generate the right sequence of header inclusions and definitions in the compilation units. The lexical ordering of program units (LPUDG) in (a) already implies the explicit dependencies among them (PUDG): the dependencies on declarations are found by clustering equivalent classes for each declarations in (b); the partial ordering of the equivalent class partitions is defined by the set inclusion relationship (c); and the header inclusions are generated by sorting with the partition ordering (d). The generated code may change the order of declarations. For example in Figure 2a, a sequence of declarations h_1, h_3, h_2 is restructured into a sequence of header inclusions H_0, H_1, where H_0, H_1 are generated headers with declarations h_3 and h_1, h_2 respectively (Figure 2c). Therefore the new compilation unit will have a new sequence of headers h_3, h_1, h_2 before inclusion expansion. Using the property 3 (see following), we can prove that the new sequence still keeps the dependencies.

For a header restructured FDG $\mathcal{G}$, the following properties hold.

1. **No false dependencies in headers.** For any two declaration program units u, v in the same generated header file, if there is a dependency path from u to a compilation unit $w \in \mathcal{C}$, there is also a dependency path from v to w.
   Proof. There is an dependency path from $u \in N$ to $w \in \mathcal{C}$ iff $w \in \mathcal{D}(u)$. By the header partitioning procedure, if u, v is in the same generated header then $\mathcal{D}(u) = \mathcal{D}(v)$. Thus $w \in \mathcal{D}(v)$, in other words, there is also a dependency path from v to w. □

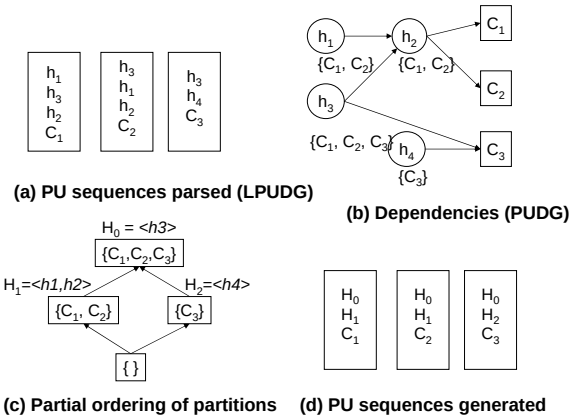

Figure 2. The illustrative steps of the header restructuring algorithm

2. **Largest granularity.** If a set containing two nodes from separate partitions is considered as a header, then a false dependency is introduced. In other words, For any two vertices u, v in two different generated headers, there is a $w \in \mathcal{C}$ such that either there is a path from u to w but no path from v to w, or there is a path from v to w but no path from u to w.
   Proof. Since u, v belong to different partitions, $\mathcal{D}(u) \neq \mathcal{D}(v)$. Either $\mathcal{D}(u) \cap \mathcal{D}(v) = \phi$ or $\mathcal{D}(u) \cap \mathcal{D}(v) \neq \phi$. If the intersection is empty, any node $w \in \mathcal{D}(u) \cup \mathcal{D}(v)$ satisfies the conclusion; if the intersection is not empty, then any node $w \in (\mathcal{D}(u) \setminus \mathcal{D}(v)) \cup (\mathcal{D}(v) \setminus \mathcal{D}(u)) = (\mathcal{D}(u) \cup \mathcal{D}(v)) \setminus (\mathcal{D}(u) \cap \mathcal{D}(v))$ satisfies the conclusion. □

3. **Correct generation of code.** The generated code respects the dependencies in the PUDG $G = (P, D)$. In other words, if there is a dependency between any two program units $a^i, b^i \in P, (a^i, b^i) \in D$ in the same compilation unit $i \in \mathcal{C}$, then they are generated in the order of a^i, b^i in the restructured code after preprocessing.
   Proof. (1) By calculation, *CompareUnit*(H^i, H^j) returns $i < j$ if $\mathcal{D}(H^i) \supset \mathcal{D}(H^j)$ and returns $i \neq j$ if $\mathcal{D}(H^i) \neq \mathcal{D}(H^j)$. (2) Also, we prove that a dependency $(a, b) \in D$ implies $\mathcal{D}(a) \supseteq \mathcal{D}(b)$: Since we have removed redundant program units, thus for any compilation unit i where b occurs, there is a definition program unit $c^i \in N^i$ such that $(b, c^i) \in D^i$. By the transitive property of dependency relations, if $(a, b) \in D^i$, then $(a, c^i) \in D^i$. In other words, $i \in \mathcal{D}(a)$. Therefore $\mathcal{D}(a) \supseteq \mathcal{D}(b)$. This establish a lattice (Figure 2(c)). (3) Now, consider a violation of the PUDG dependency happens as $(b, a) \in D$ while

$a \prec b$. If both a, b are program units in the same file, they must follow the order in the original sequence of program units by *ComparePU*, thus $b \prec a$; if b is in a generated header and a is in the definition part of the compilation unit, then $b \prec a$ by the output order of the algorithm; if they are included from two different generated headers: $a \in H^i, b \in H^j$ and $i \neq j$, since $a \prec b$ then $i < j$. However, by (1) and (2), $i \geq j$. Therefore, in any case, the violating dependency does not occur between two outputted program units in the lexical order defined by the algorithm. □

Using this algorithm, it is not necessary to place the duplicate inclusion guards around any generated header since it is only included once in each compilation unit. The time complexity of the partitioning procedure is $\mathcal{O}(|N| \times |\mathcal{C}|)$ operations.

The restructured header inclusions after false dependency removal help to accurately identify the compilation units that need to recompile as a program unit is changed, without wasting time on the compilation of falsely dependent compilation units. Therefore, header restructuring can reduce the time spent on incremental build at the expense of increasing number of generated headers.

Directory clustering Having partitioned the program units into headers, one can think of an additional optimization to partition the headers and associated compilation units together into a local directory[2].

Given the partitioned compilation units, the directory restructuring moves the generated headers along with the compilation units to the corresponding directories. 1) Convert the FDG into a list of dependency vectors on the compilation units, similar to the output from the Intel compiler `-M` option or `makedepend`. 2) Pass them to a clustering algorithm to group similar records together. As a clustering algorithm in our approach, we use LIMBO [4], which minimizes the loss of information across the clusters it builds. The outcome from the algorithm is n partitions of the compilation units. We move the compilation units and the headers into n separate directories corresponding to the partitions. The samples provided to the clustering algorithm accurately reveal the architecture and guarantee a good clustering. 3) Next, factor out the common headers used by each directory until there is no more redundancies. To save time, we only compare the common headers among all directories (in this case 1), the common headers between two directories $(n(n-1)/2)$ and the remaining distinct headers in each directory (n). Moreover, a factoring is performed on directories A and B only if $(|A \cap B|/|A \cup B|) > \epsilon$ where ϵ is a threshold with a default value of 0.5. 4) Finally preprocess the small headers in each directory into a large one and change the corresponding inclusions in every compilation unit.

This change to the FDG will introduce some false dependencies back into the program. It is a trade-off between componentizing the software system and introducing overhead for the incremental build [12].

3 Experiments

We have applied our approach to two case studies: VIM 6.2 and a mature industrial product owned by IBM (Figure 1). Our experimental results, presented in the following subsections, show that, in both cases, the improvement of our approach was significant relative to other approaches that do not use our optimizations.

3.1 Restructuring VIM

The public domain program VIM (Vi IMproved) 6.2 [29] is studied. The source code includes 61 `.c` files, 24 `.h` headers, 38 `.xpm` headers and 56 `.pro` headers[3]. The complete code base has 220 KLOC.

Prepare the code bases and the compilation farm. After running the `configure` command for Linux, `-g -O2` option and 49 compilation units were chosen as *original* code base. These units depend on 355 unique headers. Transitively, each file on average includes 290 headers. The precompilation (`-fdump-program-unit`) and restructuring(`-fdump-headers`) were done automatically using our adapted GCC 3.4.0, while the LIMBO clustering algorithm was performed after the desired number of clusters was chosen as 3 according to the number of components in Model-View-Controller (MVC) architecture [24][4]. Apart from the original code base, we obtained three additional code bases, namely the *precompiled*, *restructured* and *componentized* ones.

In Table 1, the bytes needed for storing the code base and the number of inclusion directives are compared.

The experiments on VIM were carried out on a network of Linux workstations. The host machine for the compilations is a 2.20 GHz Intel Pentium 4 workstation, with 512 KB cache. The OS is RedHat Linux 7.3, with kernel version 2.4.20-30.7 compiled by GCC 2.96. We also used the servers available in the local area network of our campus lab. The list of remote servers are show in Table 2[5]. The compilation farm can use up to 8 processors: 2 x 2.8GHz,

[2]This restructuring creates a structure similar to Java packages.

[3]Additional headers will be introduced from the inclusion of system headers

[4]The resulting clusters do follow the MVC architecture for VIM.

[5]SPECint2k benchmark index indicates the integer computation speed of the CPU, while SPECfp2k index indicates the floating point computation speed of the CPU [20].

Table 1. Header statistics.

	Original	Precompiled	Restructured	Componentized
Header	527,271	0	261,376	125,440
Compilation units	5,095,601	5,366,778	4,557,615	3,983,735
Total bytes	5,622,872	5,366,778	4,818,991	4,109,175
No. of unique headers	355	0	925	5
No. of header inclusions	14,276	0	5,778	138

Table 2. Hardware of the compilation farm

Machine	CPUs	Type	Cache	Memory	SPECint2k	SPECfp2k
host	1@2.20 GHz	P4	512KB	1024MB	770	690
A	2@2.80 GHz	P4Xeon	512KB	3072MB	907	810
B1	2@2.40 GHz	P4Xeon	512KB	6144MB	815	750
B2	2@2.40 GHz	P4Xeon	512KB	4096MB	815	750
C	1@1.60 GHz	P4	512KB	256MB	636	571

4 x 2.4GHz, 1 x 2.2GHz (the local workstation) and 1 x 1.6GHz. All machines use the same operating system, although `distcc` allows for cross-compilation. The times are measured as the average of 10 separate runs of the same settings.

Improvement of fresh builds The average size of preprocessed files was reduced from 708.9 KB to 104.71 KB. The overall build size is reduced from 33.9 MB to 5.01 MB. The size saving comparisons of individual compilation units are shown in Figure 3a. The data items are horizontally sorted by the original preprocessed file size. The similar shapes of the two curves indicate that the reduction happens almost uniformly to every compilation unit. The time savings and their comparisons are shown in Figure 3b. Here the data items are still sorted by the descending order of the original preprocess file size. In this manner, we can not only see the correlation between the curves in this chart, but also the correlation between the preprocessed file size and the compilation time. The compilation time is almost uniformly reduced for each compilation unit, since almost every unit in VIM includes `vim.h`. The net speedup by precompilation is 251% (2.51x). The precompilation overhead is needed only for the first fresh build. Taking into account precompilation overhead for the first build, the time is still 12.6% faster than the original fresh build. If the precompiled code is compiled N times, then the overhead can be divided by N. The restructured and componentized code has less time reduction than fresh build.

We also compared the compilation time for the complete program when different compiler options were used. To compare with parallel compilation, we chose `distcc`, a distributed compiler specially designed for C/C++ compilation. Other parallel compilers are general purpose and we do not intend to compare with the C/C++ compilation. To compare with compilation cache `ccache`, we used the `$HOME/.ccache` as a shared directory for the cached files. The first run is after the cleanup of the cache using `ccache -C`, the second run with `ccache` is just after the first one to utilize the cache. The third and fourth runs are associated with different parallel `make` options `-j5` and `-j20` respectively. We also compared two different C/C++ compilers GCC 2.96 (gcc) and Intel C/C++ compiler 8.0 (icc) on the Linux system[6]. The time of the fresh build using different `make` options is shown in Figure 4.

When there are available processors, in our case 8, the parallel compilation applying `distcc` leads to a speedup of 3x (3 times), far below 8 because of the network traffic in the environment. When the compiler cache or preprocessed header technique is applied for the first time, the compilation degrades by warming up the cache; when they are applied later, their net speedup over parallel build was in the range of 60%. For example, the combined speedup becomes 5x after applying the caching techniques on top of `distcc`;

Our precompilation further reduces compilation time by 1.5x to 8x further than over techniques. The highest overall speedup 39.59x is reached when all the above techniques are combined for GCC compilation, while the highest net speedup using precompilation over other techniques is 8.41x. The net speedup is higher on a heavy-loaded compiler farm for parallel build than that for sequential build because the code size reduction also reduces the network bandwidth demands by sending/receiving preprocessed images to/from remote compilers. In summary, our redundancy removal precompilation is orthogonal to parallel compilation, compilation cache and precompiled headers techniques.

Improvement of incremental builds In this experiment, we want to see whether incremental builds can be improved. The change data of VIM at each incremental build is not available[7], therefore a probability analysis is used by assuming that a code base per incremental build has changed ΔL lines of code and the probability of change for each line is uniform $\Delta L/L$ where L is the total lines of code (LOC).

Consider a file dependency graph (FDG), and measure

[6] Due to the platform chosen, we did not test the Microsoft Visual C/C++ compiler and the HP C/C++ compiler cited in the related work

[7] The publicly committed CVS log does not match the real development changes since not all changes were committed to the repository.

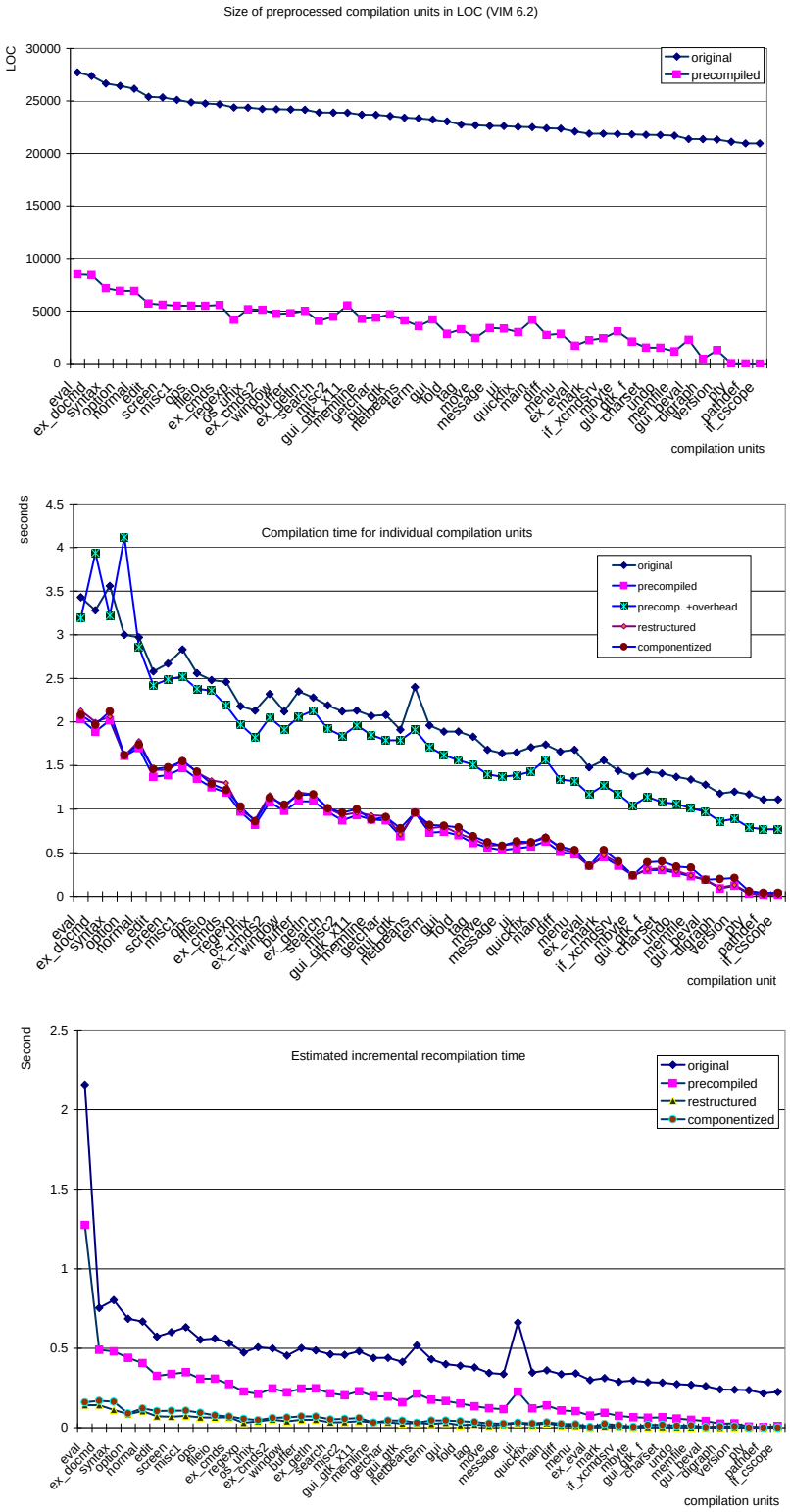

Figure 3. (a) Size, (b) Fresh build time, (c) Incremental build time for VIM code bases

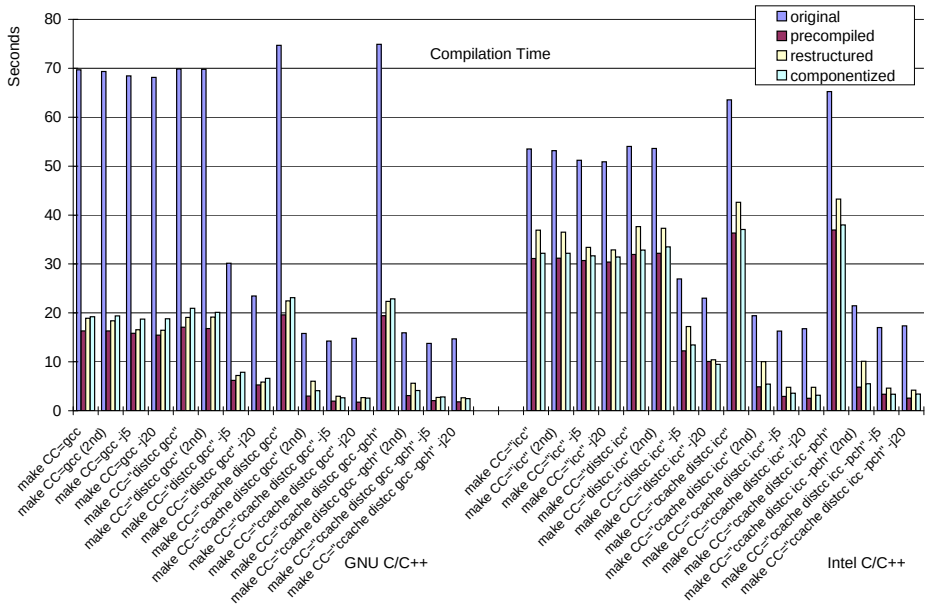

Figure 4. Fresh build time of the original, precompiled, restructured and componentized VIM.

the line of code for each file as L_{H_i} for headers H_i or L_{C_i} for compilation units C_i. The probability of changing a header H_i or a compilation unit C_i is $L_{H_i}\Delta L/L$ or $L_{C_i}\Delta L/L$ respectively. For each changed header H_i, one can expect all the dependent compilation units $\mathcal{D}(i)$ need a recompilation, whereas for each changed compilation unit, only itself will be recompiled. In the original code base, a compilation unit C_i needs a recompilation if either its implementation is changed, or any of its dependent headers is changed. If we measure the time for its recompilation as t_i, then the overall incremental build time is

$$\begin{aligned} &\Delta t = \textstyle\sum_i p_i t_i \text{where} \\ &p_i = [L(C_i) + \textstyle\sum_{j|i\in\mathcal{D}(H_j)} L(H_j)]\Delta L/L \end{aligned} \quad (1)$$

The precompiled code base uses the same FDG as the original, but Equation (1) is adjusted to Equation 2 since the directly changed compilation unit needs an overhead t'_i of redo the precompilation, while indirectly changed compilation unit can recompile quicker with the precompiled code to amortize the overhead.

$$\begin{aligned} &\Delta t = \textstyle\sum_i [p^c_i(t_i + t'_i) + (1 - p^c_i)p^h_i t_i] \quad \text{where} \\ &p^c_i = L(C_i)\Delta L/L \\ &p^h_i = \textstyle\sum_{j|i\in\mathcal{D}(H_j)} L(H_j)\Delta L/L \end{aligned} \quad (2)$$

For the restructured and componentized code base, equation 1 is used, since the restructuring and clustering needs to be done only once during the incremental build. However, a smaller parameter L and a reduced FDG were used.

Having the LOC of source files (in Figure 3a) and the timing of the compilation units (in Figure 3b), the incremental build analyses of the *original*, *precompiled*, *restructured* and *componentized* code bases are shown in Figure 3c: for each compilation unit, the estimated recompilation time per incremental build is calculated using Equation (1) for the *original*, *restructured* and *componentized* program or using Equation (2) for the *precompiled* program. In total, for the *original*, *precompiled*, *restructured* and *componentized* code base, an incremental build when changing one line of code takes respectively 22.73, 10.06, 1.76 and 2.46 seconds of recompilation (see Figure 3c), whereas their fresh build including linking takes 97.89, 39.04, 41.1 and 40.91 seconds respectively.

Testing We verified that both the header restructured and componentized VIM programs have the same functionalities as the original program using the 51 test cases accompanying the VIM source code under `testdir` directory. Among them, the original VIM succeeded in 49 test cases except for a test case for "gf" (case 17) and a test case "insert expansion" (case 32). According to documentation in VIM, they are designed for testing VIM on the WIN32 platform. It is worth noting that the restructured and the componentized VIM also succeeded in the 49 test cases and failed in the same 2 test cases.

3.2 Restructuring an industrial program

The overall gain in the build time, though significant, may not impact productivity significantly. For instance, in the VIM case study, the amount of activity and the absolute value of the build time is not large enough to justify the restructuring effort. To conduct a more realistic evaluation of

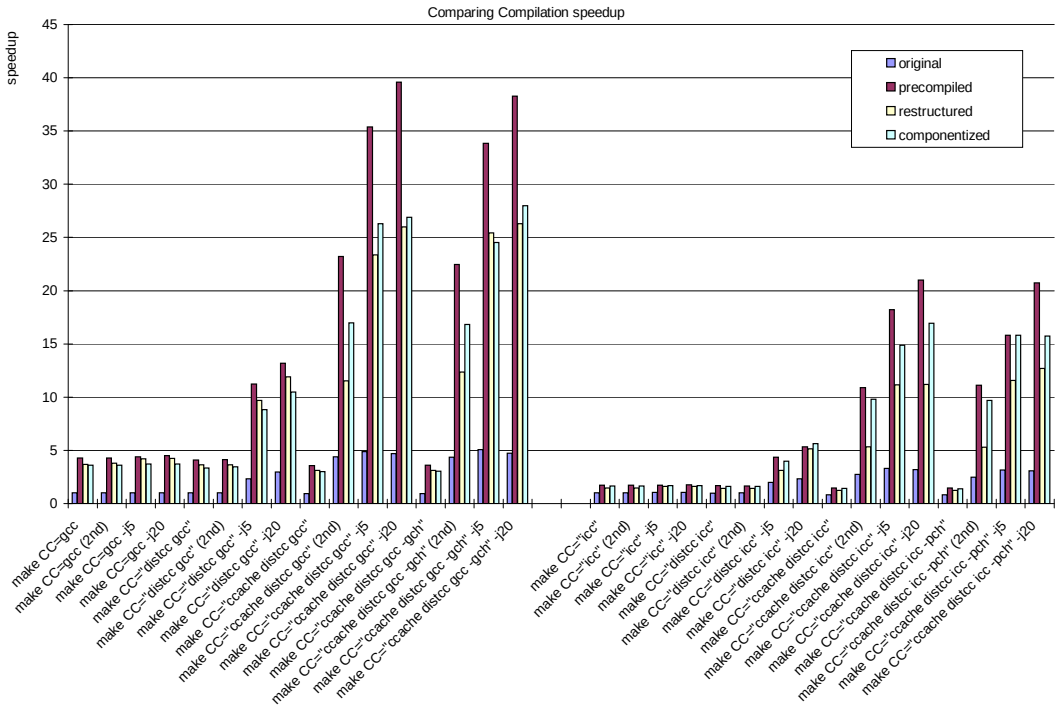

Figure 5. Speedup of different build options over the default build

Table 3. Metrics of the industrial software code base and the studied component

metrics	no. files	no. LOC	no. bytes
compilation units in all components	3620	6,070,719	235,979,237
headers used in all components	2644	1,417,780	55,936,320
compilation units in our component	187	172,505	7,151,821
included headers in our component	871	492,983	20,189,245
included system headers	139	23,234	816,432
included user headers	683	469,749	19,372,813
after preprocessing	187	12,770,714	378,732,475
after precompilation	187	7,331,121	187,791,633

our proposal, we applied it to shrink-wrap software product [8] with 112 components (organized as directory of files) and over 7 million lines of C++ code (Figure 1).

Our approach was applied to one of the C/C++ components under study. Its metrics is shown in Table 3. Over the years, the header dependencies in the code-base have decayed to the extent that each program file in our component, on average, includes 543 headers (directly or indirectly). The average size of each compilation unit is around 37 KB, expanding 53x to around 1.96 MB after inclusions. Though the component under study only has 172KLOC in its compilation units, or 2.8% of the system, the distinctly included headers have 20MB, almost 34.7% of all the distinct headers in the system. The average build time from scratch – including the preprocessing – for this component is around 19 minutes. This number reduces to around 14 minutes if the files are preprocessed. Applying precompilation alone, the preprocessed size reduction was 182.1MB, or 50.42%. Figure 7a breaks it down into compilation units ordered by preprocessed size. The build time was 4.35 minutes, saving 9.57 minutes over preprocessed program. The break down of reduction time is shown in Figures 7b, also ordered by preprocessed size. The linear trend line of the reduction time shows how reduction is distributed.

[8] Due to confidentiality issues, we cannot disclose the software name.

4 Related work

Two major categories of performance tuning techniques exist: those which transforms the program to increase the parallelism and utilize locality; others which updates the algorithm to remove unnecessary computations. They can apply to the compilation optimization, as a compiler can be regarded as an application program.

Figure 8 highlights our fine-grain precompilation in the complete compilation processes of GNU CC.

A `Makefile` declares a set of dependency rules between targets [17]. Only the targets that transitively depends on a more recent target will be executed. The `make` *targets optimization* finds truly dependent targets and removes unnecessary ones in the transitive closure of the end target [18, 37]. Unless the code to compile is generated dur-

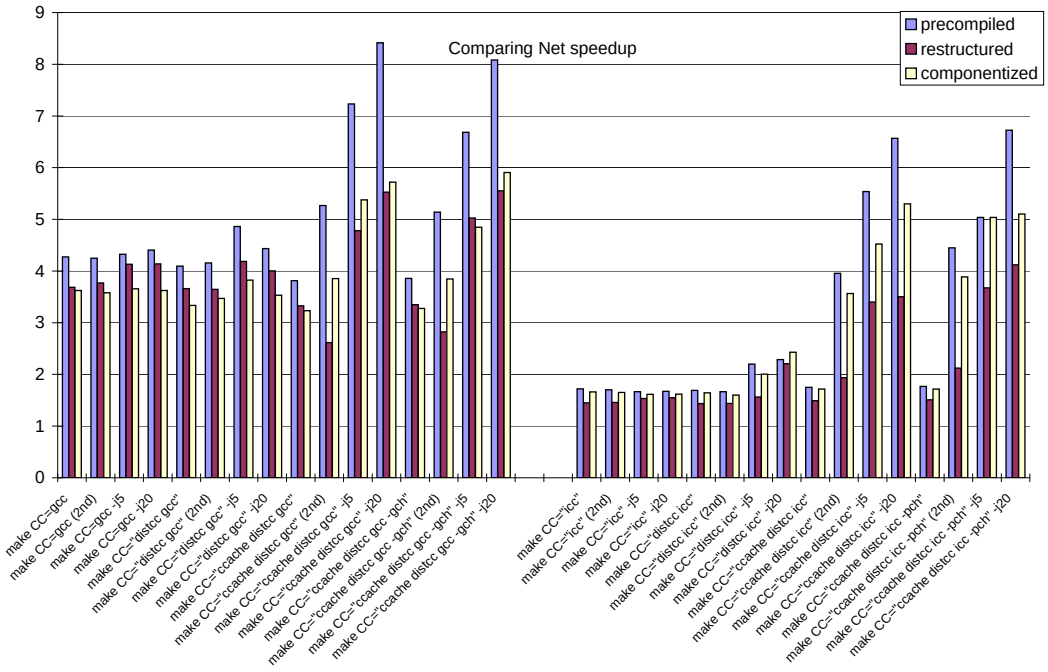

Figure 6. Net speedups of precompilation, header restructuring and componentization on top of various optimization techniques.

Table 4. An analogy of program performance tunings between execution and compilation

Concept	Execution speed	Compilation speed
Parallelism	parallelization [3, 41, 42]	parallel compilation [18, 13, 14, 15, 37, 34, 33]
Locality	caching [5, 7] prefetching [30]	compilation cache [32, 23, 38] precompiled headers [28, 25, 36, 31, 19]
Redundancy	dead code elimination [2]	target removal [37], selective recompilation [9, 1] header restructuring [40] precompilation (this work)

ing the build, most compilations can be fully *parallelized* across different compilation units. Thus if the development machine has multiple processors, a '-jN' option for `make` can fork N processes to do the compilation tasks at the same time[9]. Using a network of workstations (NOW), `pmake` [13], `pvmmake` [14], `mpimake` [15], `dmake` [37], `lsmake` [33] and `distcc` [34] all aim at dispatching parallel compilation jobs to a set of workstations. In particular, `distcc` is a parallel C/C++ compilation tool that utilize the available workstations in a `compilation farm`.

Usually parallel compilation tool should work along with a *caching* mechanism for a compilation. It takes much more time for a compiler to *preprocess* an input file and expand them into a stream of text for parsing, than to load the preprocessed file directly. Thus the preprocessed file can be stored in a cache to speedup the preprocessing. Hashing the cached entries can help locate the preprocessed files stored in the cache even faster. `ccache` [38] implements the compiler cache by placing the preprocessed files into a directory where each of them are hashed and shared, similar technique [23, 32] does caching within a server compiler.

The *precompiled header* (PCH) option is implemented in modern C/C++ compilers to cache the compiled headers

[9] Even on a single processor system, forking two parallel tasks usually outperforms sequential make because the CPU can be better utilized rather than waiting for the I/O devices.

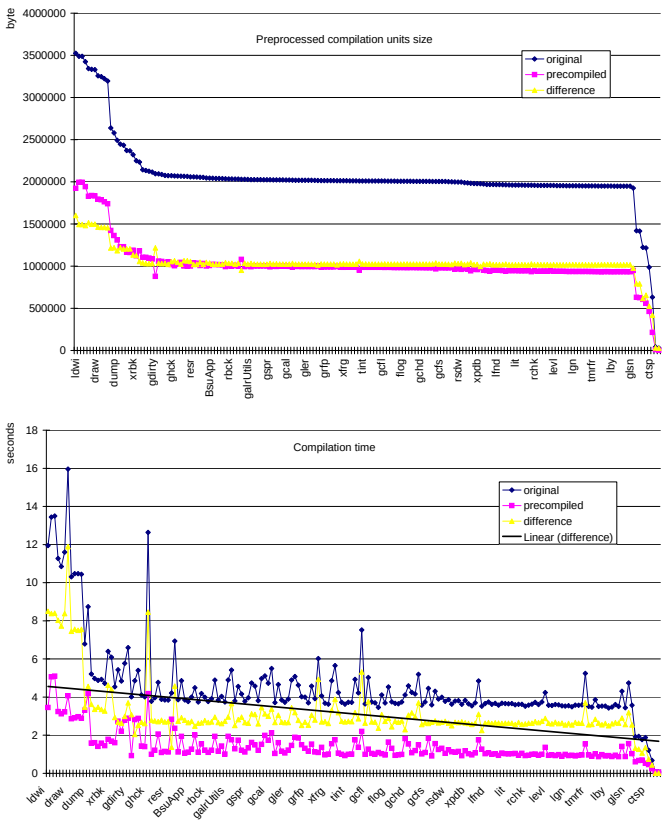

Figure 7. Size (a) and time (b) reduction for the component.

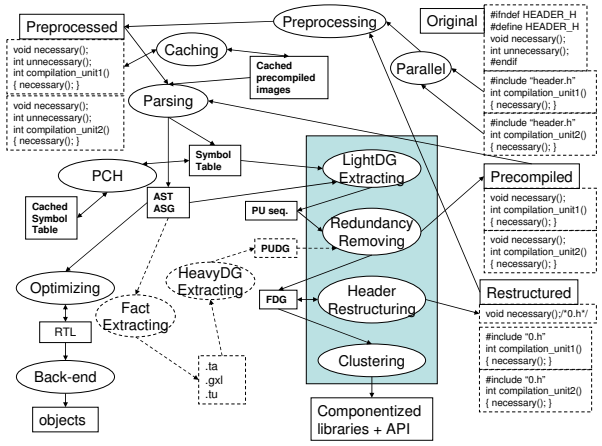

Figure 8. Precompilation process

in order to reuse the compilation result[10]. Different from `ccache`, PCH techniques deal with headers only and the cached results are in object form, thus the cached headers can not be shared among different compilers. Unlike including all program units in the headers by the PCH approach, our header restructuring selectively includes the program units that are necessary for the compilation units. The program unit dependence graph is much finer than the file dependencies, thus lead to an additional speedup to the PCH. Our precompilation result is in source form, ready to share among different C/C++ compilers.

A concept related to false dependency is the Ratio of Use to Visibility (RUV) [8]. Here *Use* defines the number of compilation units where a declaration is used and *Visible* defines the number of compilation units where the declaration is used. RUV can be seen as an indicator of false dependencies. After our header restructuring, the ratio will be restored to 1. In [1], the cost to various recompilation techniques was surveyed. The *cascading* recompilation triggers recompilation whenever a change to the `make` target happens; the *surface* recompilation does not trigger a *cascading* recompilation when changes are made to comments; the *cutoff* recompilation triggers a *cascading* recompilation only when changes are made to preprocessed images. The *smart* recompilation in [1] triggers a *cascading* recompilation only when a change is made to the smallest file dependency graph derived from the headers. Unlike us, these techniques do not restructure the headers to reflect the true dependencies, rather it maintains a dependency graph using existing headers, thus the RUV they obtained was still below 1; the link-time *smartest recompilation* has to rely on the type inference to generalize types of undeclared identifies, and as noted by the author, it may be counterproductive because it slows the error removal.

Elsewhere [40] we presented an algorithm to remove all false dependencies through header restructuring. The algorithm relies on 3rd party parsers (also called *fact extractors*) such as CPPX [22], Datrix [6, 21], KLOCwork [35] or the `-fdump-translation-unit` option in GCC [19], to prepare a cumbersome *abstract syntax graph*, which records all the direct symbol relations in a relational tuple format. In [40], we developed a dependency extractor based on the array of detailed entities generated from one of the specialized fact extractors. The speed of the *heavy-weight dependency extraction* was slow due to the large number of excessive entities and relations extracted. For example, during the compilation of VIM 6.2 (Section 3), we obtained 72,056 various dependencies among 22,489 program units, whereas the fact extractor in Datrix would report 3,008,664 various relations among 1,852,326 entities.

[10]Commercial compilers have implemented precompiled headers as an advanced option, for example: Microsoft Visual C/C++'s `/YX` option [31] generates precompiled headers as `*.pch` files, Intel C/C++ compiler's `-pch` option [36] generates `*.pchi` files. So does HP ANSI C/C++ compiler [25] and an NeXT implementation [28] where a detail explanation the mechanism in the precompilation can be found. Starting from version 3.4.0, GNU GCC can also precompile headers into `*.gch` files [19].

With the same objective to remove redundancies and false dependencies, this paper reports efficient algorithms to extract a sequence of program units along with parsing implemented in our adapted GCC compiler. Comparing with the explicit program unit dependency graph, the program units sequence has less complexity (lighter-weight) and result in an efficient precompilation and header restructuring. In addition, the precompiled or restructured code base are smaller than the precompiled headers as well as the preprocessed files. Our precompilation results are compiler-independent since the results are in source form and can be reused by other C/C++ compilers. The adapted GCC compiler is also transparent to the *make* process as the precompilation is implemented into a `-fdump-program-unit -fsyntax-only` option and the header restructuring as an additional `-fdump-headers` option.

5 Conclusion

This paper presented a fine-grain redundancy removal precompilation technique for speeding up the compilation of large C/C++ programs. Experimental results have shown that this technique is orthogonal to other optimization techniques such as parallel compilation, caching and precompiled headers. Apart from improving the fresh build process, we also presented a light-weight fine-grain header restructuring technique that can achieve efficient incremental build. The overhead of preprocessing the generated headers can be further reduced by a clustering-based componentization. By adapting the GCC compiler to include our precompilation as its options, no change is needed on the existing `Makefile`. Experiments showed that it can achieve up to 8 times gain over the speed of compilation already tuned with parallelism and locality and a large-scale C/C++ software can apply this precompilation technique. The algorithm is not limited to C/C++, e.g., the tool can be combined with gSOAP [16] to restructure integrated WSDL interfaces as they are equivalent to the headers generated by its `wsdl2h` tool.

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